

01-02-02

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## EXPRESS MAIL CERTIFICATE

I hereby certify that the above paper/fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 CFR 1.10 on the date indicated below and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231, December 27, 2001. Express Mail No.: EL744463099US. Signature of Person mailing paper/fee:

Joseph A. Sawyer

THE COMMISSIONER OF PATENTS AND TRADEMARK  
Washington, DC 20231

Attorney Docket: G0727/2243P

PATENT

Sir:

Transmitted herewith for filing is the Patent Application of

Inventor(s): Mark RANDOLPH

For: PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR IMPROVED V<sub>SS</sub>  
RESISTANCE AND FOR PROCESS SIMPLIFICATION

J1002 U.S. PTO  
10/032646  
12/27/01

Enclosed with the Patent Application are:

- ☒ Three (3) sheet(s) of drawings  
☒ Declaration of Inventor(s)  
☒ Power of Attorney by Assignee  
☒ Assignment and Recordation Form  
☒ Certification and Request for Non-Publication (35 USC 1.22(b))  
☒ (2) Self Addressed, Stamped Postcard

The filing fee has been calculated as shown below:

|                                    | (Col. 1)  | (Col. 2)  |  |
|------------------------------------|-----------|-----------|--|
| FOR:                               | NO. FILED | NO. EXTRA |  |
| BASIC FEE                          |           |           |  |
| TOTAL CLAIMS                       | 10        | - 20 = 0  |  |
| INDEP. CLAIMS                      | 3         | - 3 = 0   |  |
| MULTIPLE DEPENDENT CLAIM PRESENTED |           |           |  |

\*If the difference in Col. 1 is less than "0", enter "0" in Col. 2

## LARGE ENTITY

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☒ The Commissioner is hereby authorized to charge payment of \$740.00 and the following fees associated with this communication or credit any overpayment to Deposit Account 01-0365 (AMD, Inc.). A duplicate copy of this sheet is attached.

☒ Any additional filing fees required under 37 CFR 1.16.

☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

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|  |                      |   |
|--|----------------------|---|
| <b>REQUEST AND CERTIFICATION<br/>UNDER<br/>35 U.S.C. 122(b)(2)(B)(i)</b> | First Named Inventor | Mark RANDOLPH   |
|  | Title                | PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR IMPROVED V <sub>SS</sub> RESISTANCE AND FOR PROCESS SIMPLIFICATION |
|  | Atty Docket Number   | G0727/2243P   |

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

12/27/01

Date

Signature

Joseph A. Sawyer, Jr., Reg. No. 30,801  
Attorney for Applicant(s)

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of such filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**